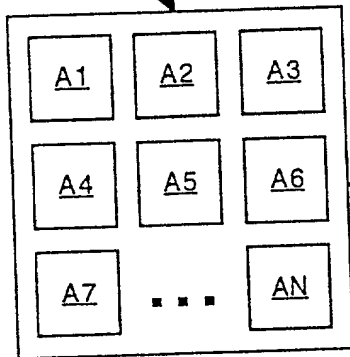


Fig. 1

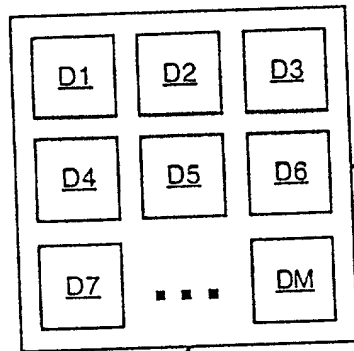
ANALOG BLOCKS
230

25



INTRA-BLOCK
ROUTING
235

PROGRAMMABLE
INTERCONNECT
250



DIGITAL BLOCKS
240

INTERNAL I/O
BUS 236

Figure 2A

270

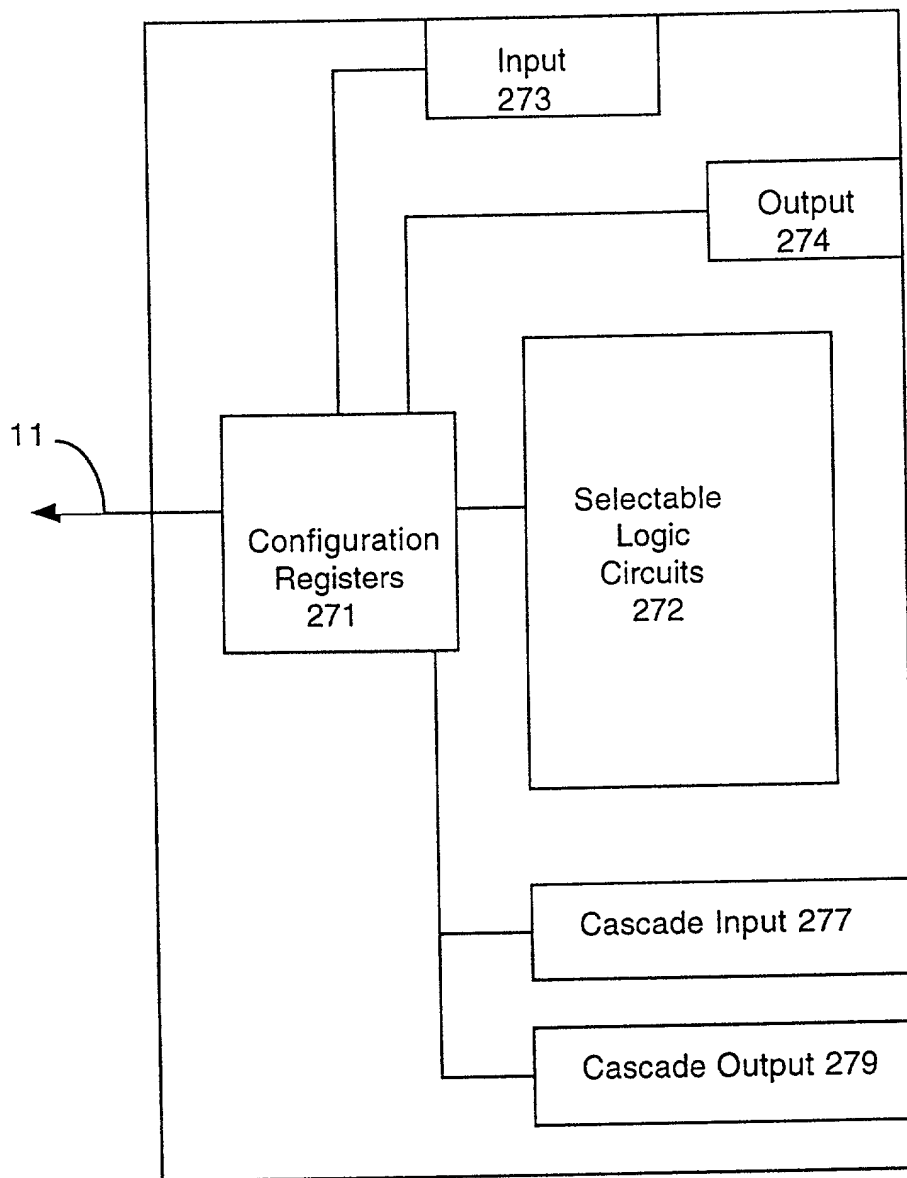


FIG 2B

300

310 ↘

Loading a plurality of images in a PSoC memory.

320 ↘

Configuring a PSoC in accordance with a first configuration image.

330 ↘

Performing functions in accordance with the first configuration image.

340 ↘

Reconfiguring a PSoC in accordance with a second configuration image.

350 ↘

Executing functions in accordance with the second configuration image.

FIG 3

400

410 ↘

Providing an interface for selecting applicable user modules.



420 ↘

Generating application files automatically.



430 ↘

Facilitating programming of the desired functionality for a target device.



440 ↘

Executing an assembler process.



450 ↘

Building a target device within the PSoC tool.



460 ↘

Emulating a target device configuration.



470 ↘

Loading a configuration image generated by the PSoC Tool into memory of a PSoC target device.

FIG 4